EE 508 Lecture 24

Integrator Design

TA-C Integrators Other Integrator Structures

Review from last time

Integrator Characteristics of Interest

$$X_{\text{IN}} = \frac{I_0}{s}$$

$$X_{\text{OUT}}$$

$$I(s) = \frac{I_0}{s}$$

Properties of an ideal integrator:

$$|I(j\omega)| = \frac{I_0}{\omega}$$
$$\angle I(j\omega) = -90^0$$
$$|I(jI_0)| = 1$$

Gain decreases with $1/\omega$

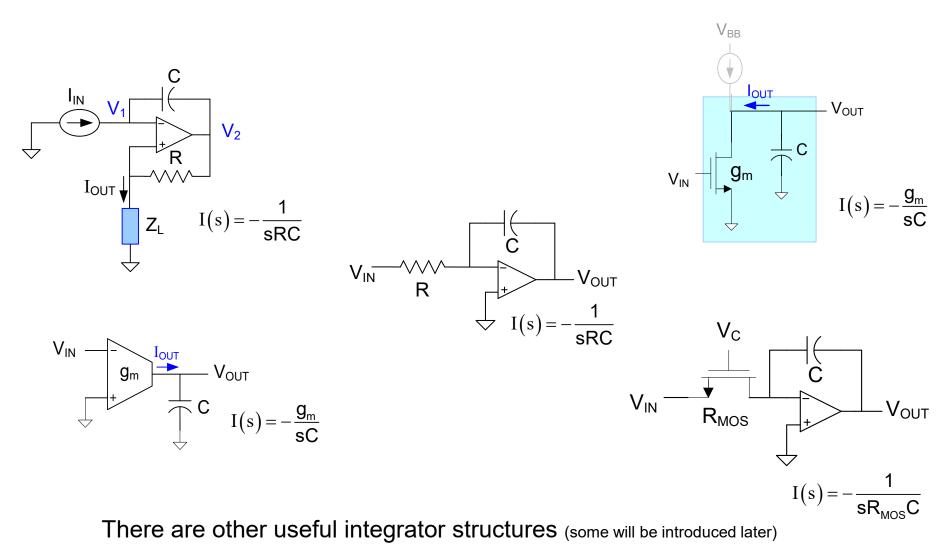
Phase is a constant -90°

Unity Gain Frequency = 1

How important is it that an integrator have all 3 of these properties?

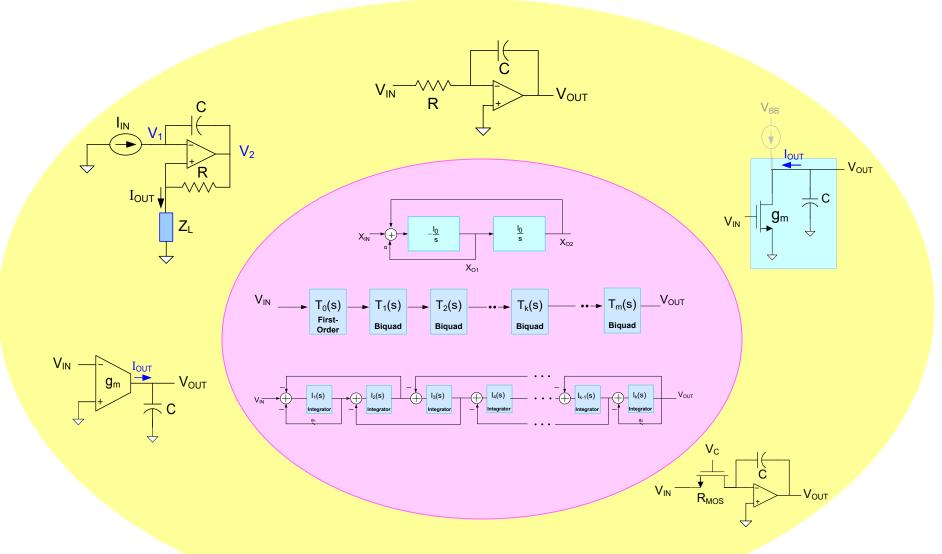
Review from last time

Some integrator structures



There are many different ways to build an inverting integrator

Review from last time Integrator-Based Filter Design



Any of these different types of integrators can be used to build integrator-based filters

Review from last time

Are new integrators still being invented?

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Searching US Patent Collection...

 Results of Search in US Patent Collection db for:

 TTL/integrator. 551 patents.

 Hits 1 through 50 out of 531

 Next 50 Hits

 Jump To

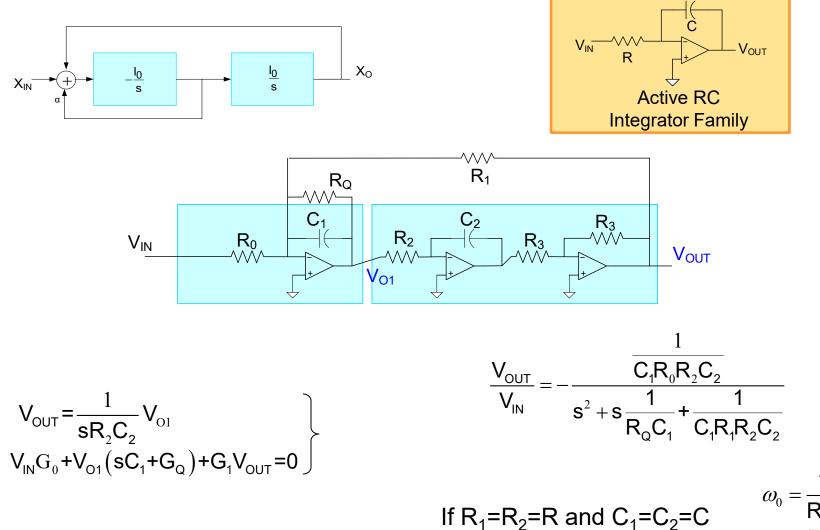
Refine Search TTL/integrator

Oct 16 2018

Review from last time

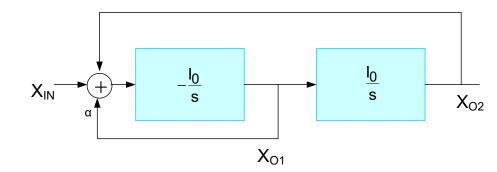
PAT. NO.	Title
1 10,082,922	2 II Increasing the dynamic range of an integrator based mutual-capacitance measurement circuit
	1 Capacitive fingerprint sensor with integrator
	2 🏛 Inverting amplifier, integrator, sample hold circuit, ad converter, image sensor, and imaging apparatus
	Gated CDS integrator
	Pregame electronic commerce integrator
	Output range for interpolation architectures employing a cascaded integrator-comb (CIC) filter with a multiplier
	Illumination optical apparatus having deflecting member, lens, polarization member to set polarization in circumference direction, and optical integrator
	Illumination optical apparatus, exposure apparatus, and exposure method with optical integrator and polarization member that changes polarization state of light
	Low power switched capacitor integrator, analog-to-digital converter and switched capacitor amplifier
	Confirming the identity of integrator applications
	Integrator and A/D converter using the same
	System integrator and method for mapping dynamic COBOL constructs to object instances for the automatic integration to object-oriented computing systems
	T Analog/digital converter with charge rebalanced integrator
	Semiconductor device including integrator and successive approximation register analog-to-digital converter and driving method of the same
	T Feedback integrator current source, transistor, and resistor coupled to input
	Signal processing apparatus for processing time variant signal with first and second input signals comprising a weighting integrator, a magnitude detector and a gain-adjustable amplifier
	T Shell integrator
	Projector having a rod integrator with an entrance plane smaller than an area light source
	Apparatus for overload recovery of an integrator in a sigma-delta modulator
	Increasing the dynamic range of an integrator based mutual-capacitance measurement circuit
	Integrator, AD converter, and radiation detection device
	T Integrator, delta-sigma modulator, and communications device
	Multi-mode discrete-time delta-sigma modulator power optimization using split-integrator scheme
	Cascaded integrator-comb filter as a non-integer sample rate converter
	Electronic integrator for Rogowski coil sensors Shell integrator
	Sent integrator Sampling network and clocking scheme for a switched-capacitor integrator
	Confirming the identity of integrator applications
	T Integrator and touch sensing system using the same
	System integrator and method for mapping dynamic COBOL constructs to object instances for the automatic integration to object-oriented computing systems
	System integrator and method for mapping dynamic COBOL constructs to object instances for the automatic integration to object-oriented computing systems Double integrator pulse wave shaper apparatus, system and method
	Analog integrator system and method
	T Dynamic current source for amplifier integrator stages
	Low power and compact area digital integrator for a digital phase detector
	Integrator for class D audio amplifier
	Illumination system having first and second lens arrays including plano-convex lenses wherein some lenses in the second array include a first and a second lens element, projection-type display apparatus, and optical integrator
	Apparatuses, methods and systems for a universal payment integrator
	DC-DC converter controller apparatus with dual-counter digital integrator
	Charge balancing converter using a passive integrator circuit
	Delta-signa modulator with reduced integrator requirements
	Compensation filter for cascaded-integrator-comb decimator
	System integrator and system integration method with reliability optimized integrated circuit chip selection
	Therapeutic integrator apparatus
	Increasing the dynamic range of an integrator based mutual-capacitance measurement circuit
	Active integrator for a capacitive sense array
	Current amplifier circuit, integrator, and ad converter
	Apparatuses and method of switched-capacitor integrator
	Image: Systems and methods for preventing saturation of analog integrator output
	System integration and method for mapping dynamic COBOL constructs to object instances for the automatic integration to object-oriented computing systems
	System integration and integration wind up for speed control in a vehicle Image: Control of the automatic integration to object-oriented computing systems
20 2,102,090	- one-sided deterior and disability of integrator wind up for speed control in a venice

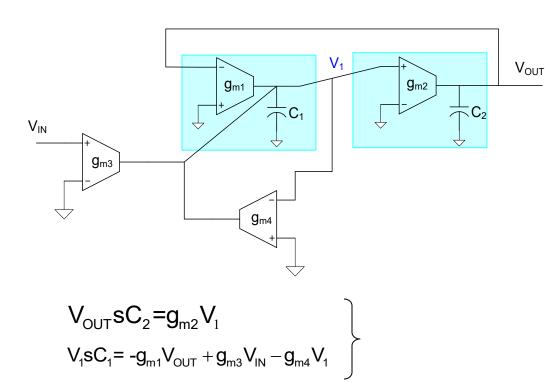
Example – Active RC Feedback Tow Thomas Biquad

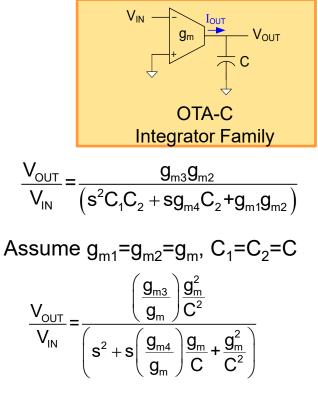


 $\omega_0 = \frac{1}{RC}$ $Q = \frac{R_Q}{R}$

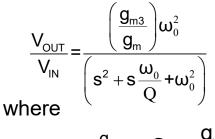
Example – OTA-C Tow Thomas Biquad





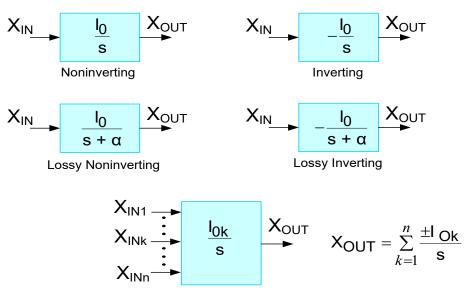


express as

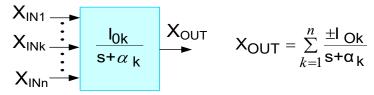


 $\omega_0 = \frac{g_m}{C} \qquad Q = \frac{g_m}{g_{m4}}$

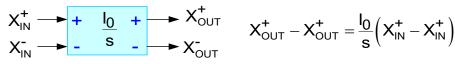
Basic Integrator Functionality (for all families)



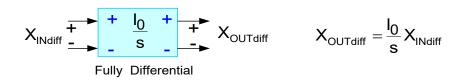
Summing (Multiple-Input) Inverting/Noninverting



Summing (Multiple-Input) Lossy Inverting/Noninverting



Balanced Differential

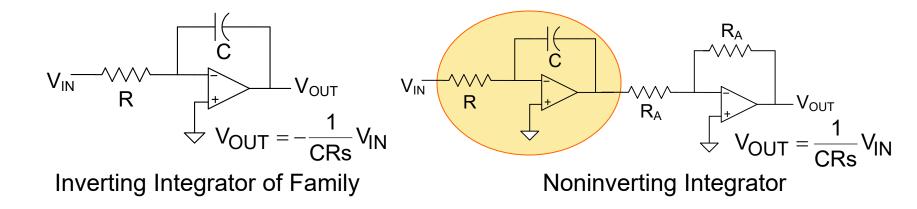


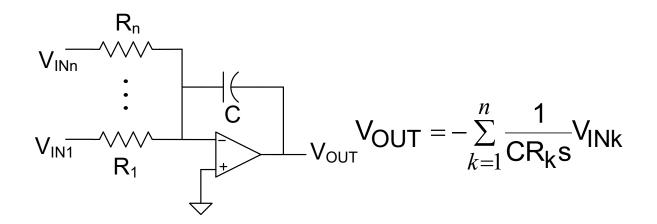
Basic Integrator Functionality



- An inverting/noninverting integrator pair define a family of integrators
- All integrator functional types can usually be obtained from the inverting/noninverting integrator pair
- Suffices to focus primarily on the design of the inverting/noninverting integrator pair since properties of class primarily determined by properties of integrator pair

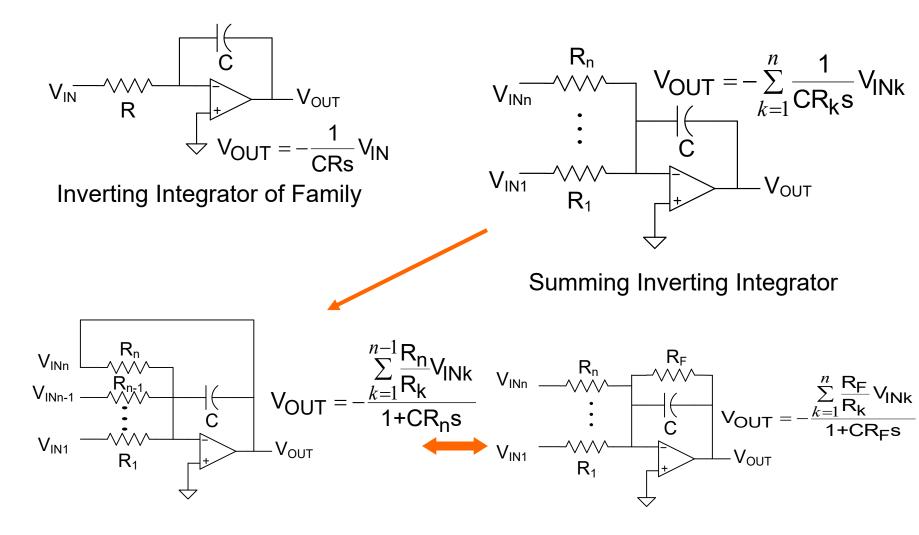
Example – Basic Op-Amp Feedback Integrator Family





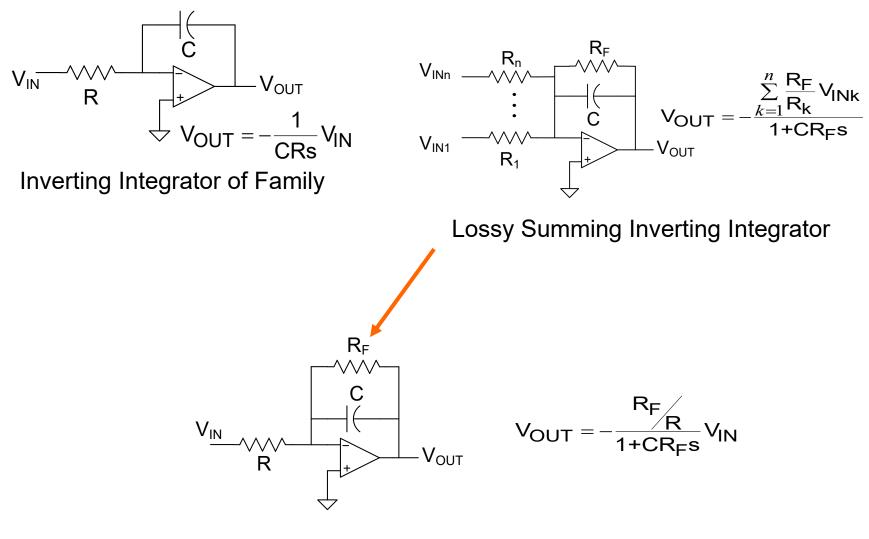
Summing Inverting Integrator

Example – Basic Op-Amp Feedback Integrator Family



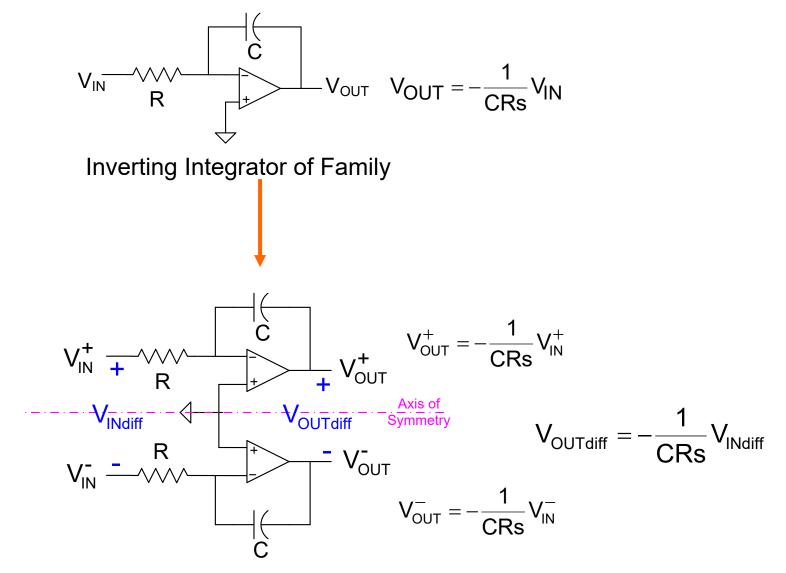
Lossy Summing Inverting Integrator

Example – Basic Op-Amp Feedback Integrator



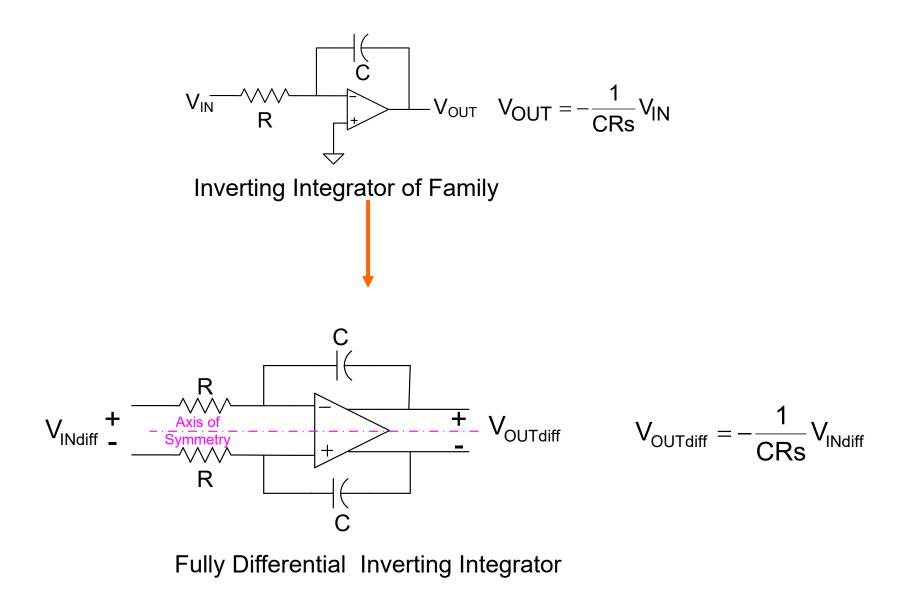
Lossy Inverting Integrator

Example – Basic Op-Amp Feedback Integrator Family



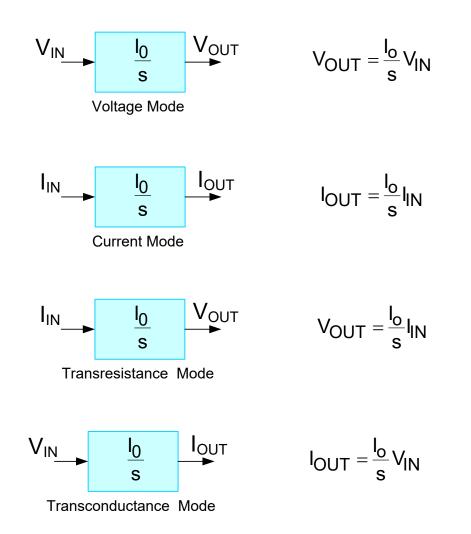
Balanced Differential Inverting Integrator

Example – Basic Op-Amp Feedback Integrator Family



Note distinction between fully balanced and fully differential structures !

Integrator Types



Will consider first the Voltage Mode type of integrators

Voltage Mode Integrators

Active RC (Feedback-based)

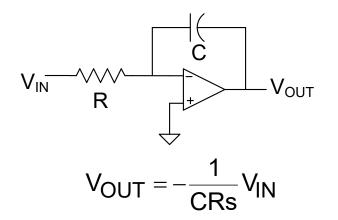
- MOSFET-C (Feedback-based)
- OTA-C
- TA-C

Sometimes termed "current mode"

- Other Continuous-time Structures
- Switched CapacitorSwitched Resistor

Discrete Time

Active RC Voltage Mode Integrator



- Limited to low frequencies because of Op Amp limitations
- No good resistors for monolithic implementations
 Area for passive resistors is too large at low frequencies
 Some recent work by Haibo Fei shows promise for some audio frequency applications
- Capacitor area too large at low frequencies for monolithic implementatins
- Active devices are highly temperature dependent, proc. dependent, and nonlinear
- No practical tuning or trimming scheme for integrated applications with passive resistors

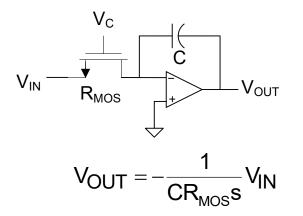
Voltage Mode Integrators

- Active RC (Feedback-based)
- MOSFET-C (Feedback-based)
 - OTA-C
 - TA-C

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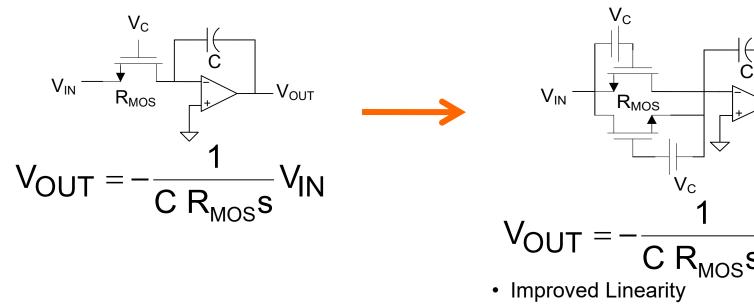
- Other Continuous-time Structures
- Switched CapacitorSwitched Resistor

Discrete Time



- Limited to low frequencies because of Op Amp limitations
- Area for R_{MOS} is manageable !
- Active devices are highly temperature dependent, process dependent
- Potential for tuning with $\rm V_{\rm C}$
- Highly Nonlinear (can be partially compensated with cross-coupled input

A Solution without a Problem



- Some challenges for implementing V_C

V_{OUT}

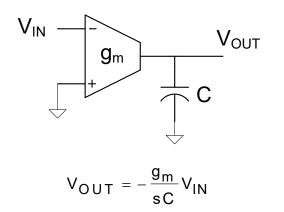
Voltage Mode Integrators

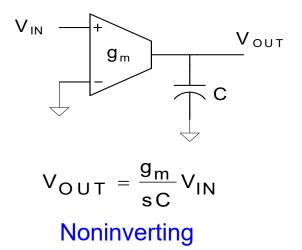
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Discrete Time

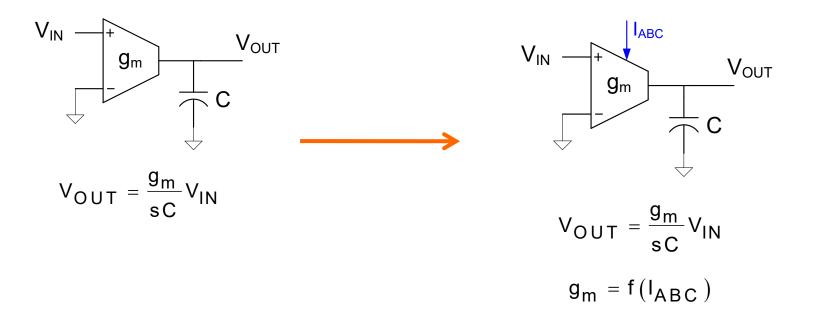




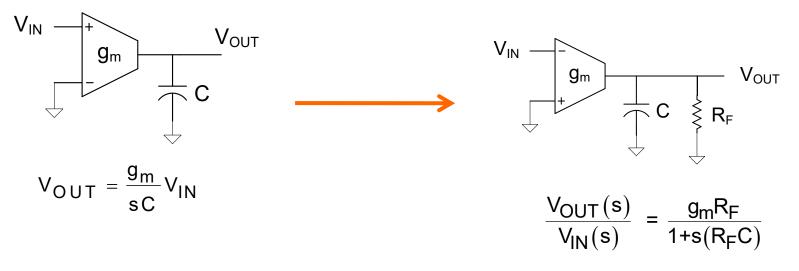
Inverting

- Requires only two components
- Inverting and Noninverting structures of same complexity
- Good high-frequency performance
- Small area
- Linearity is limited (no feedback in integrator)
- Susceptible to process and temperature variations
- Tuning control can be readily added

Widely used in high frequency applications

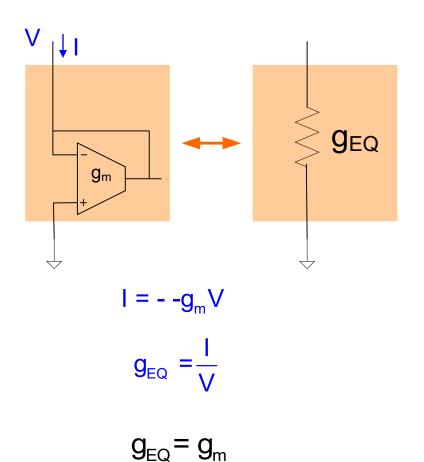


Programmable Integrator

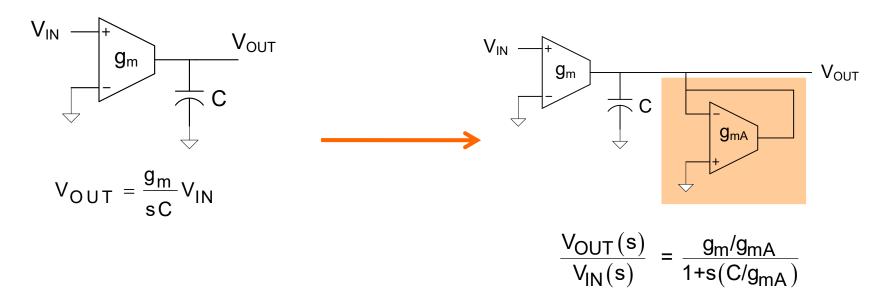


Lossy Integrator

But R_F is typically too large for integrated applications

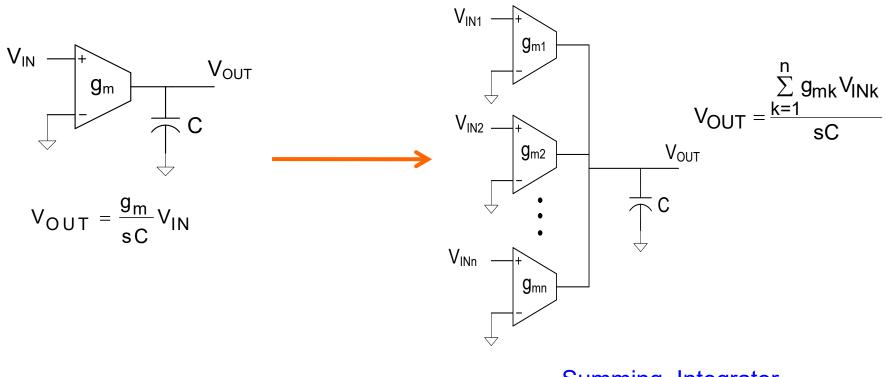


OTA is generally much smaller than a resistor



Lossy Integrator

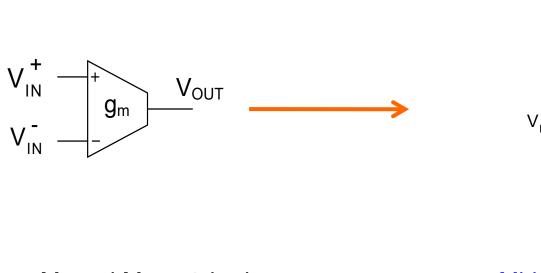
- Practical implementation
- Both OTAs can be readily programmable



Summing Integrator

- Inverting and noninverting functions can be combined in single summer
- All transconductance gains can be programmable

OTA Architecture



• M_1 and M_2 matched

Mid-complexity OTA

M₁

 M_3

 V_{DD}

 M_2

 M_4

V_{OUT}

- M₂ and M₄ matched
- Define M to be the gain of the current mirror formed with M₂ and M₄
- g_m programmable with V_{BIAS}

$$g_m = \frac{g_{m1}}{2} (1+M)$$

Often M=1

Other OTAs exist, considerable effort expended over past two decades on OTA design

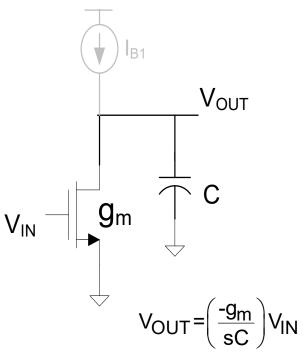
Voltage Mode Integrators

- Active RC (Feedback-based)
- MOSFET-C (Feedback-based)
- OTA-C
- ◆ TA-C

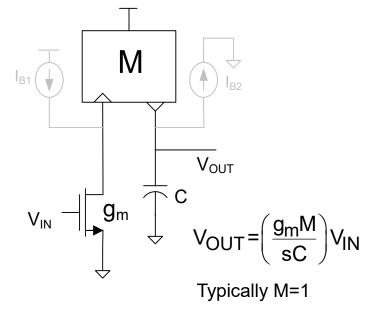
Sometimes termed "current mode"

- Other Continuous-time Structures
- Switched CapacitorSwitched Resistor

Discrete Time



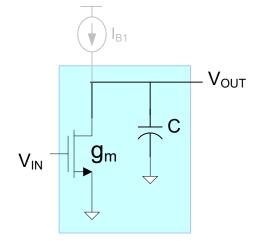


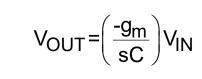


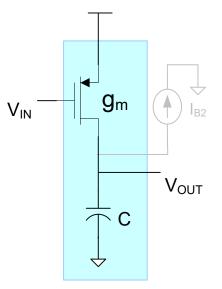
Noninverting Integrator

- Can operate at very high frequencies
- Low device count circuit
- Simplicity is important for operating at very high frequencies
- I₀ is process and temperature dependent
- Linearity is limited

Some other perspectives



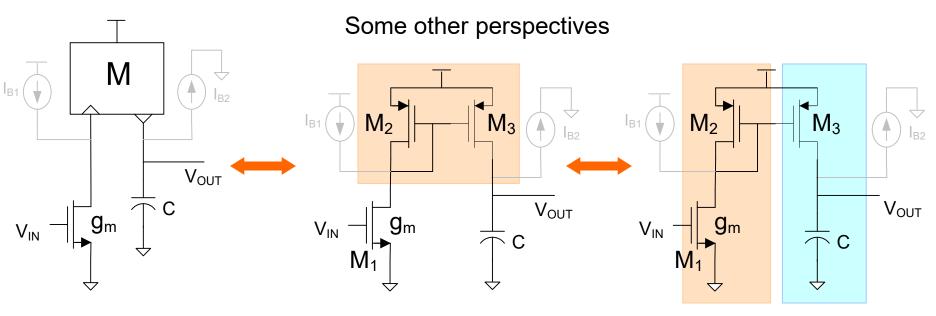




n-channel input

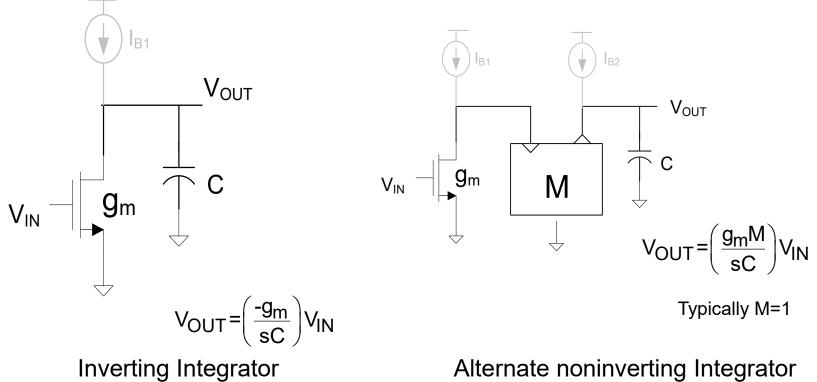
p-channel input

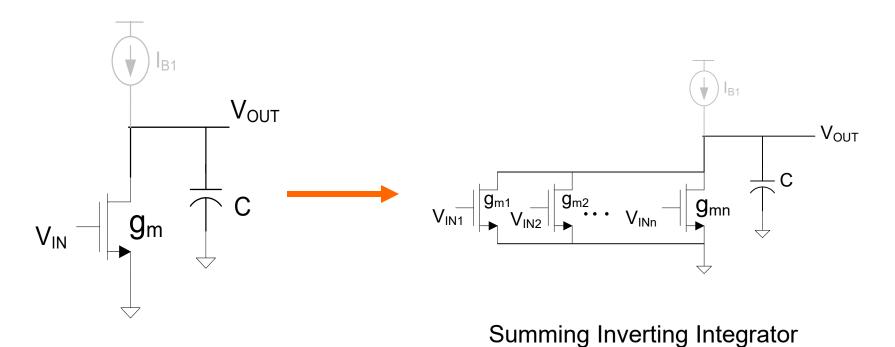
Inverting Integrators



Noninverting Integrator

Can be viewed either as n-channel input with current mirror or as low-gain inverter driving a p-channel input inverting integrator





Voltage Mode Integrators

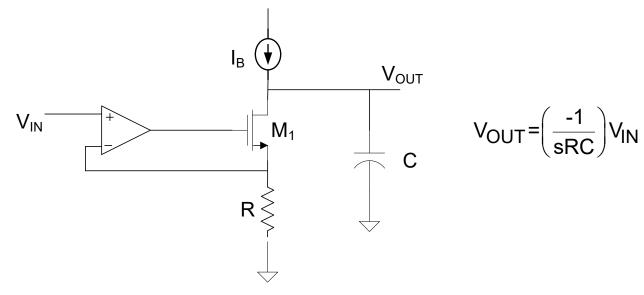
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Discrete Time

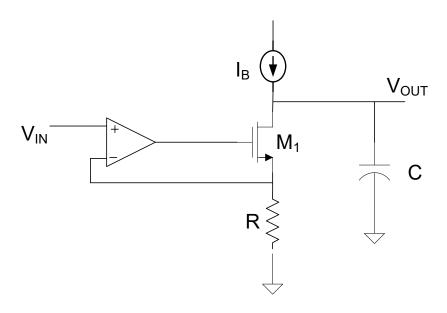
Another Voltage Mode Integrator



Inverting Integrator

- Infinite input impedance (in contrast to basic Active RC Integrator)
- Both R and C have one terminal grounded
- Requires integrated process
- Accuracy limited by process and temperature
- Size limitations same as basic Active RC Integrator
- Limited to lower frequencies because of Op Amp
- Good linearity

Another Voltage Mode Integrator



 V_{DD} W_{DD} W_{DD} W

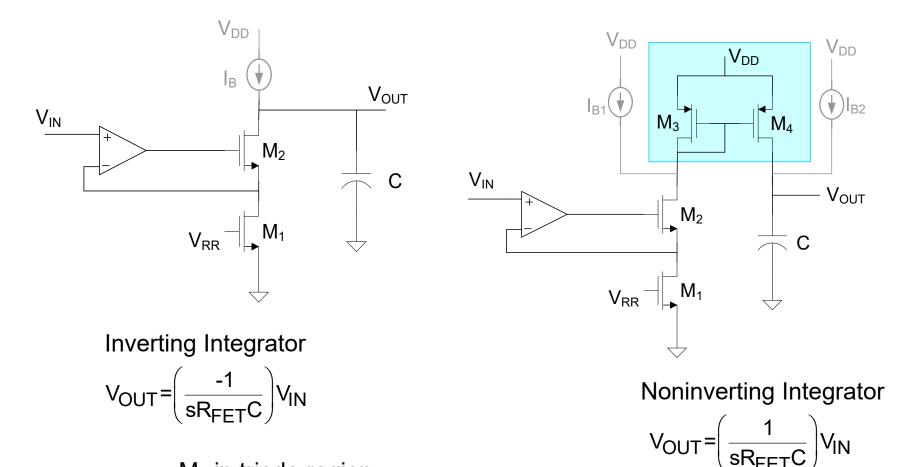
Inverting Integrator

$$V_{OUT} = \left(\frac{-1}{sRC}\right) V_{IN}$$

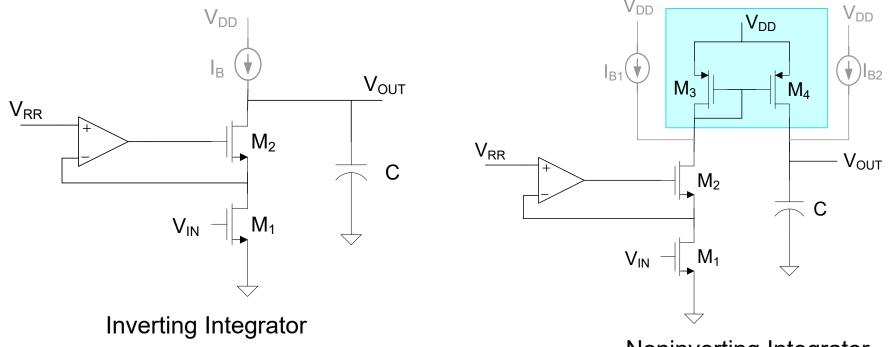
Noninverting Integrator

$$V_{OUT} = \left(\frac{1}{sRC}\right) V_{IN}$$

Another Voltage Mode Integrator



- M₁ in triode region
- Reduces Area Concerns but Loss of Linearity
- + I_0 is programmable with V_{RR}
- Accurate control of I_B critical

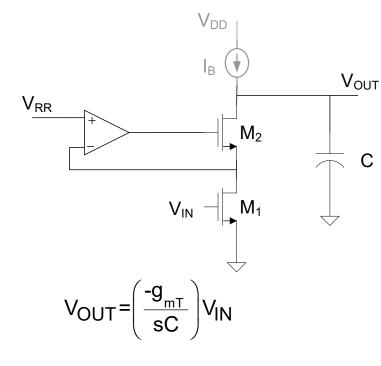


 $V_{OUT} = \left(\frac{-g_{mT}}{sC}\right) V_{IN}$

Noninverting Integrator $V_{OUT} = \left(\frac{g_{mT}}{sC}\right) V_{IN}$

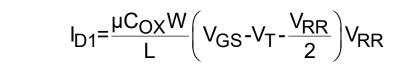
 g_{MT} is triode region transconductance of M_1

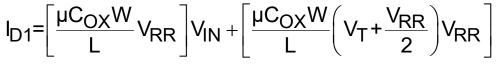
- M₁ operating in triode region
- R_{FET} programmable with V_{RR}
- Very good linearity properties
- Input impedance still infinite



Linearity Properties:

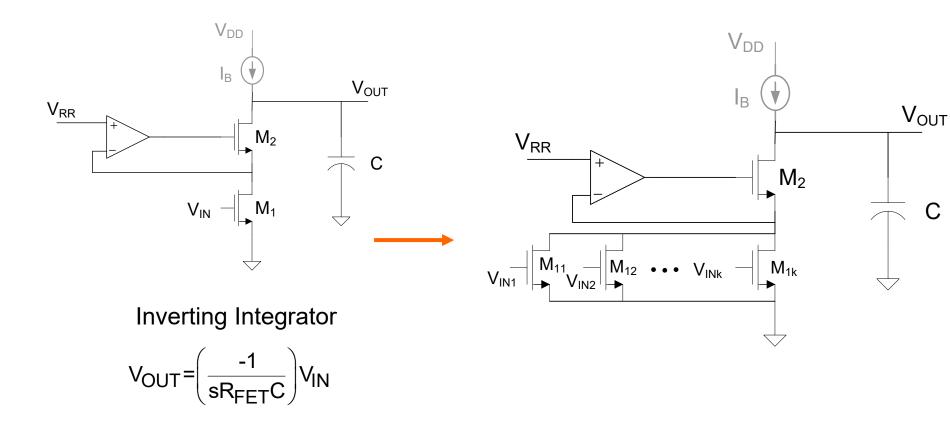
Assuming square-law triode model



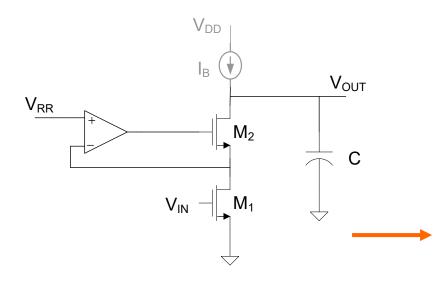


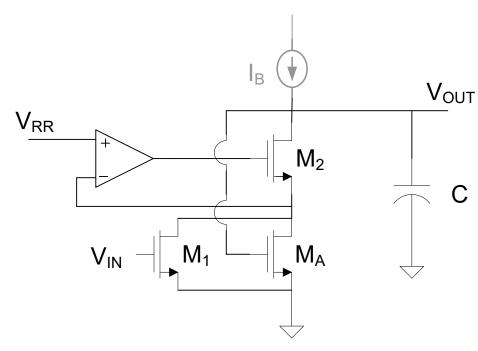
Note linear dependence on V_{IN}

$$g_{mT} = \left[\frac{L}{\mu C_{OX} W V_{RR}}\right]$$

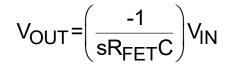


- Multiple inputs require single additional transistor
- Accurate ratioing of gains practical
- Can also sum currents on C





Inverting Integrator



Inverting Lossy Integrator



Stay Safe and Stay Healthy !

End of Lecture 24